

Form PTO- 1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	Att. Docket Number : 28944/40109	Serial number : 10/500,061
	Applicant : CHAMPION	
	Filing date : June 24, 2004	Group :

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	PATENTEE/APPLICANT	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JL	A1	US 6,154,095	11/28/00	Shigemori et al.	331	16	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLIC. DATE	COUNTRY/AGENCY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
JL	B1	WO 01/24357	04/05/01	WIPO				

Other Documents (Including Author, Title, Date, Pertinent Pages, Place of Publication, Etc.)

JL	C1	International Search Report of PCT/FR02/04433 dated April 15, 2003
JL	C2	Perrott et al., "A 27-MW CMOS FRACTIONAL-N SYNTHESIZER USING DIGITAL COMPENSATION FOR 2.5-MB/S GFSK MODULATION" <i>IEEE Journal of Solid-state Circuits</i> , IEEE INC. New York, US, Vol 32, No.12, 12/01/97, pages 2048-2060

Examiner <i>M. Packer</i>	Date Considered <i>12/02/05</i>
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EXAMINER : Initial if citation is considered, whether or not citation is in conformance with MPEP 609; Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.